FORM PTO-1449 (REV.7-80)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			ATTY. DOCKET NO. 500996.01		application no. 09/965,240			
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V9	AA	5,639,684	06/17/97	Kwok		437	60			
٧٩	AB	5,866,938	02/02/99	Takagi et al.		257 530				
JY	AC	6,458,645 B2	10/01/02	DeBoer et al.		438	240			
74	AD	6,515,343 B1	02/04/03	Shroff et a	al.	257	530			
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FOREIGN PATENT DOCUMENTS										
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	OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.) Kim, J. et al., "High Performance Antifuse with Planar Double Dielectrics on Sil-xGex Pad for									
٧५	AP	Field Programmable Gate Array Applications", IEEE Electronic Letters, Vol. 32, No. 24,								
		pp. 2276-2277, November 21, 1996.								
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* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in										
conformance and not considered. Include copy of this form with next communication to applicant(s).										

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